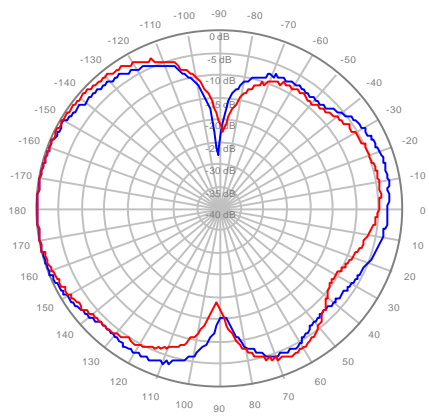
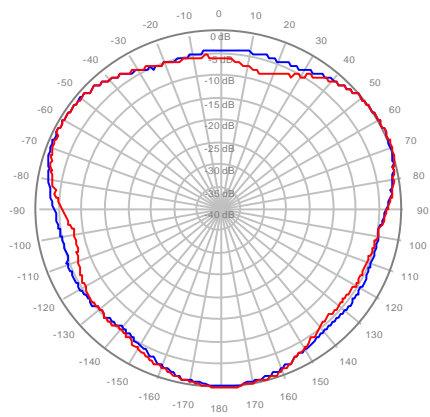
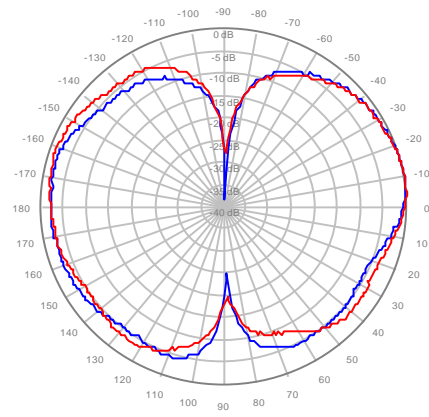
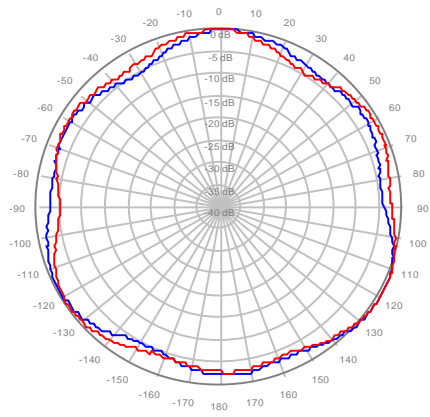


2C4U3MT360X06Fxys0 Patterns
26October2018

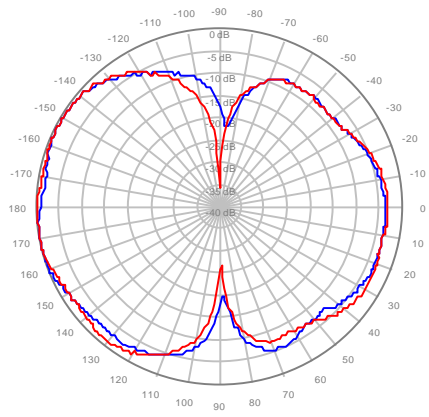
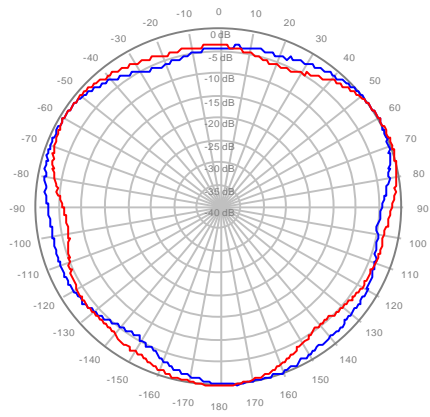
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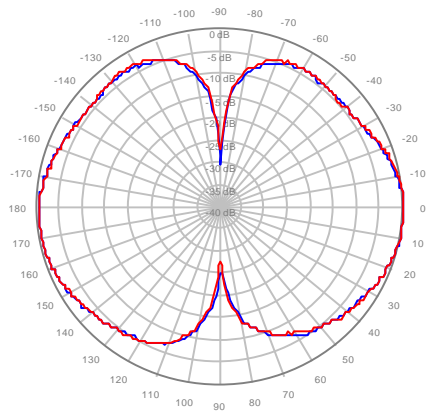
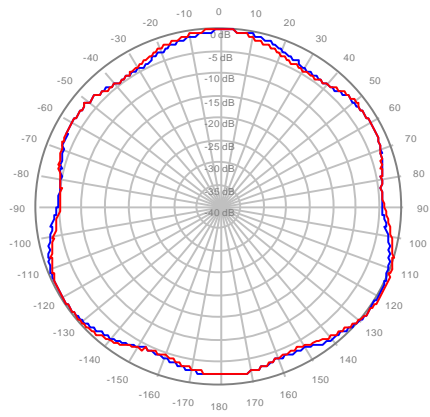
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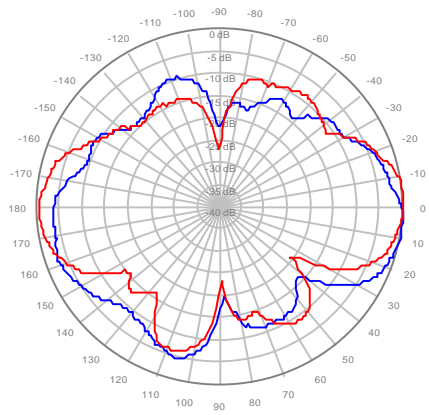
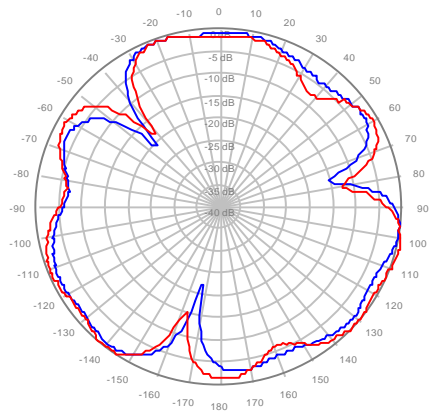
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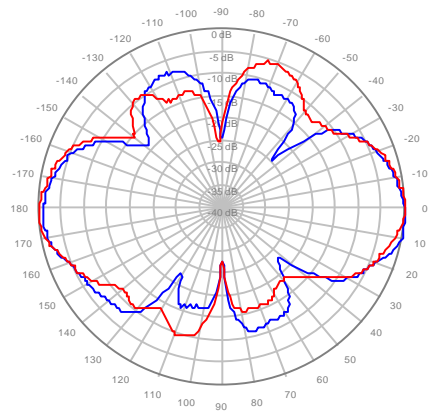
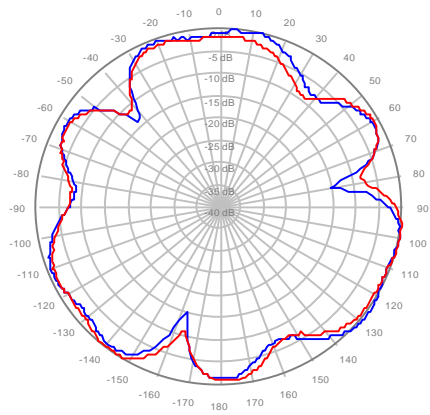
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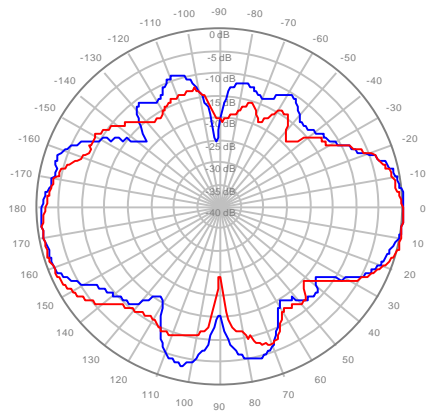
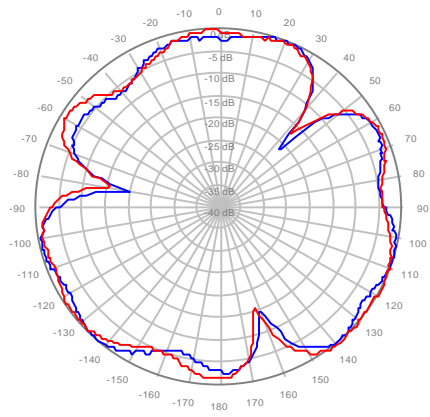
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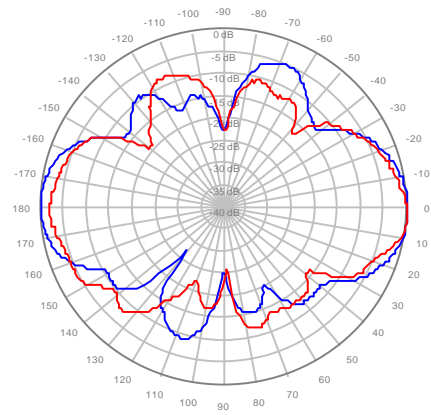
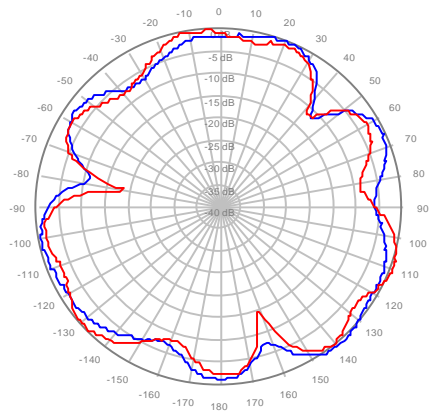
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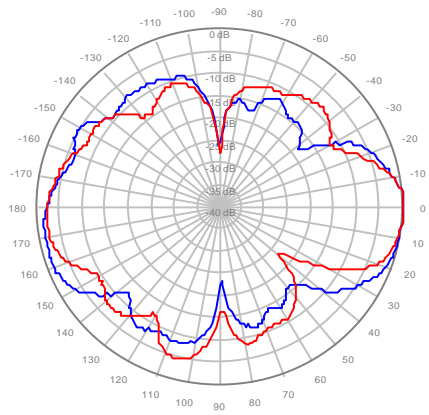
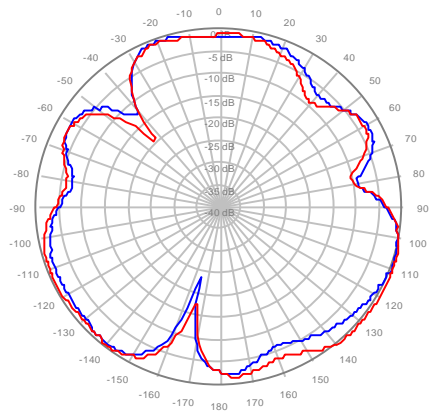
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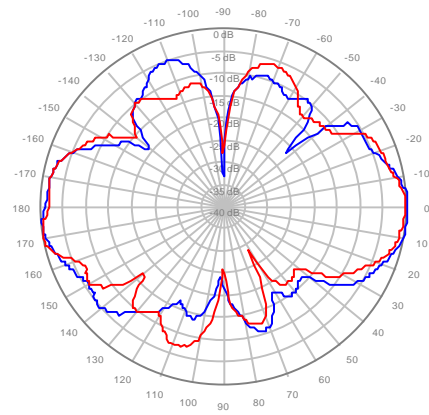
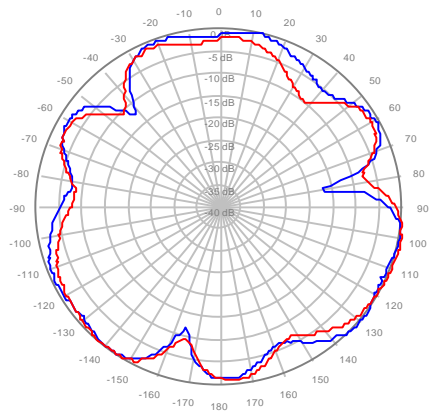
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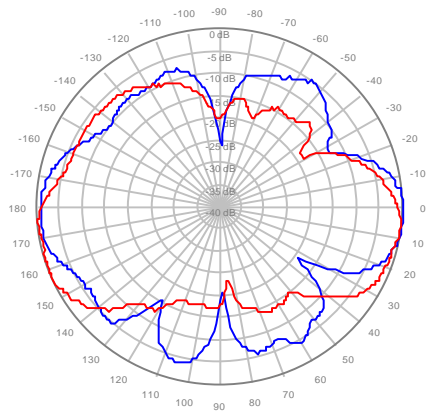
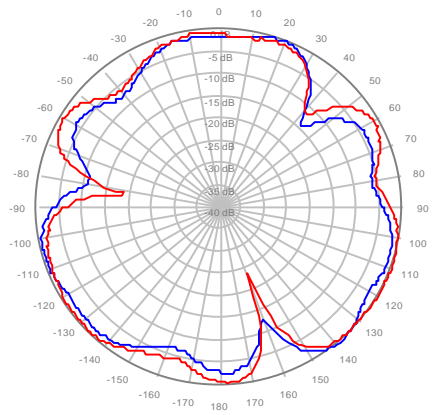
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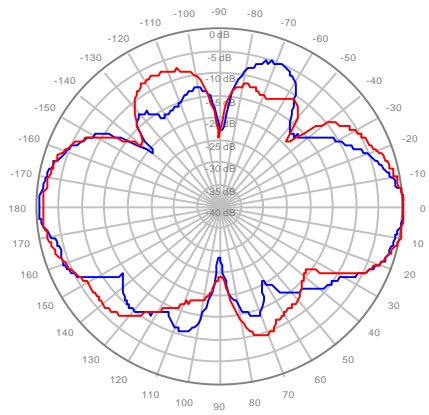
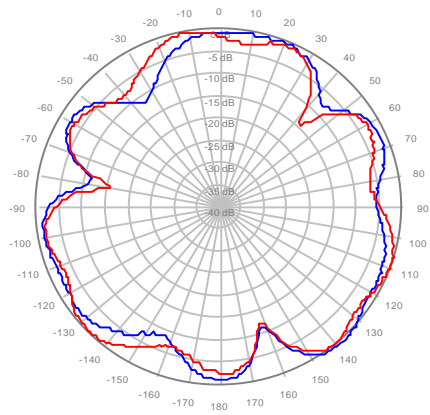
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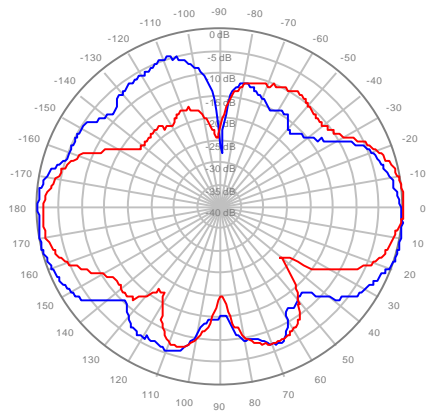
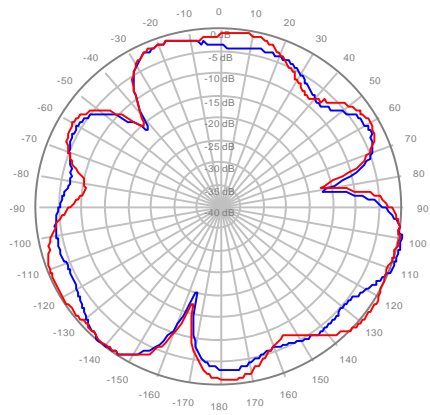
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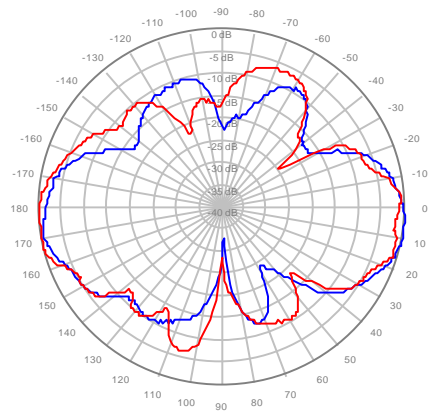
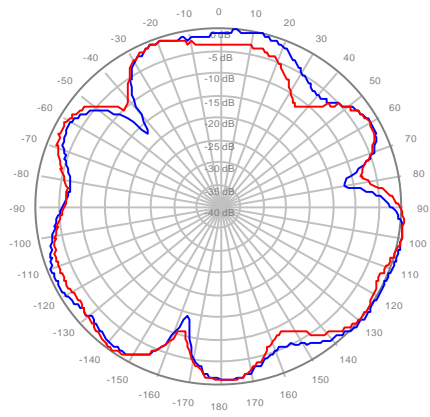
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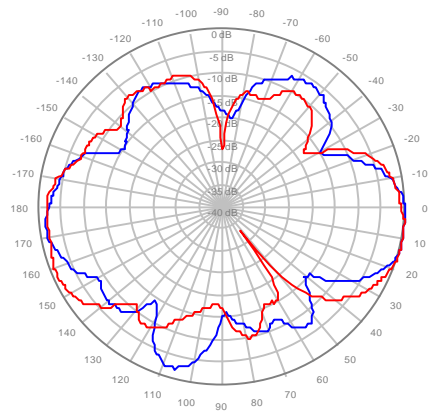
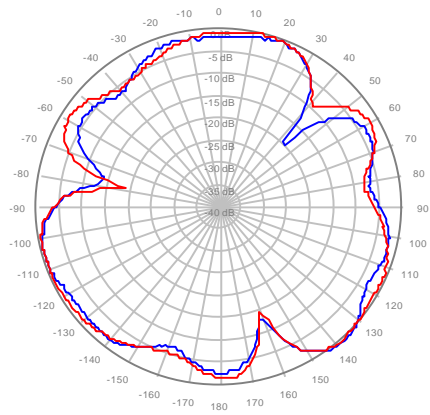
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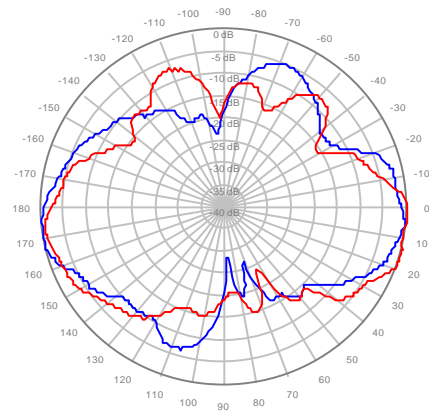
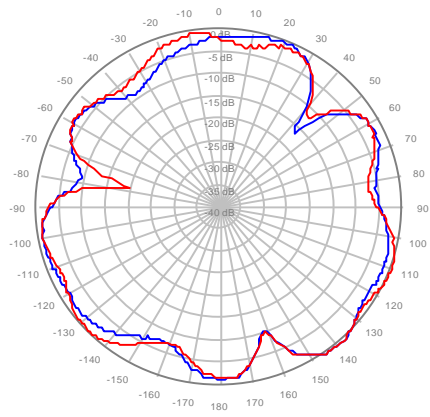
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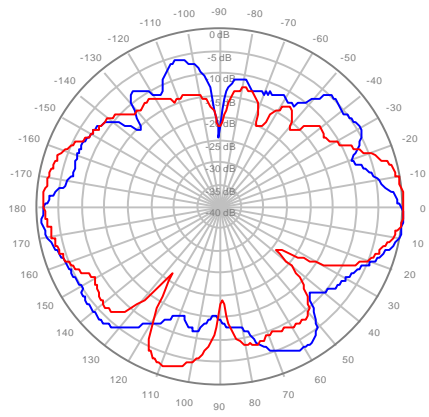
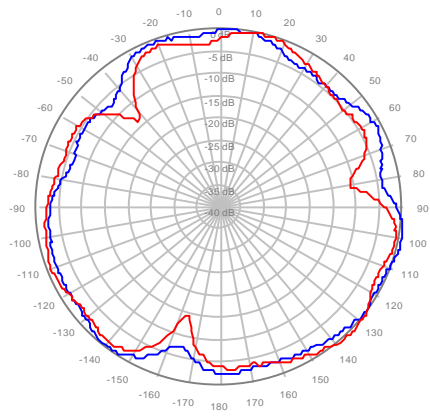
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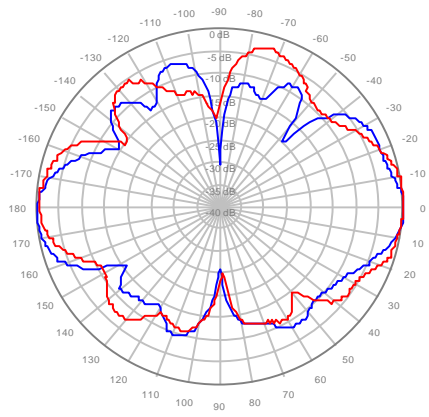
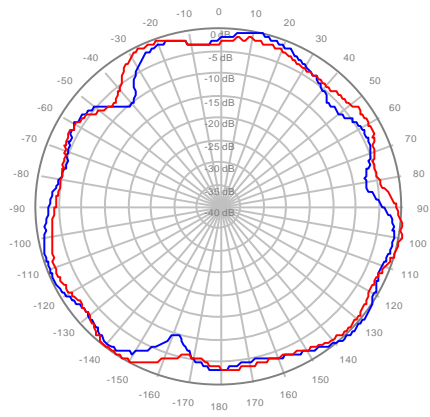
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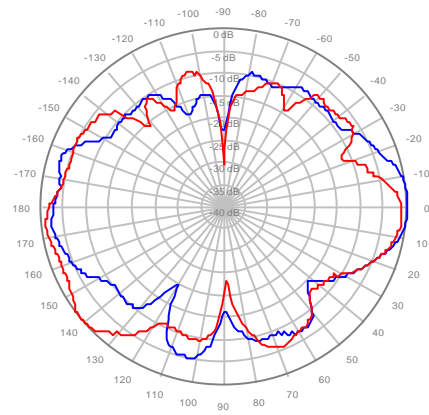
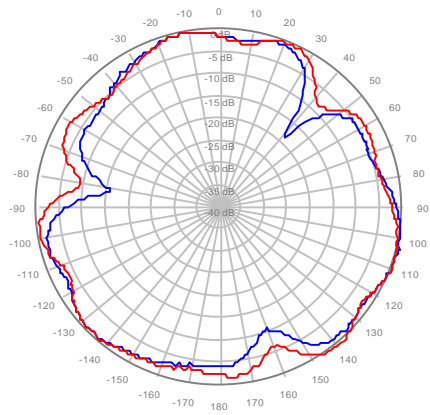
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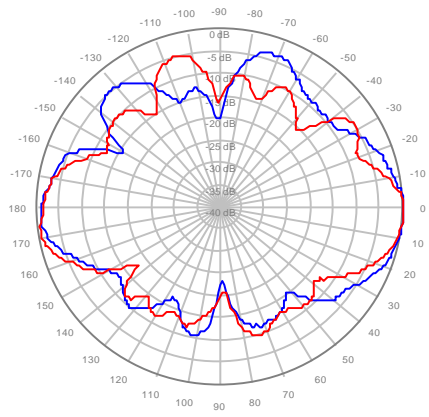
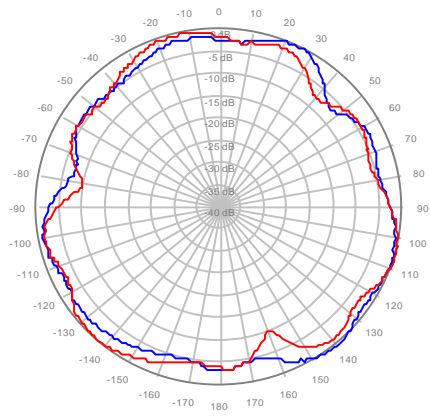
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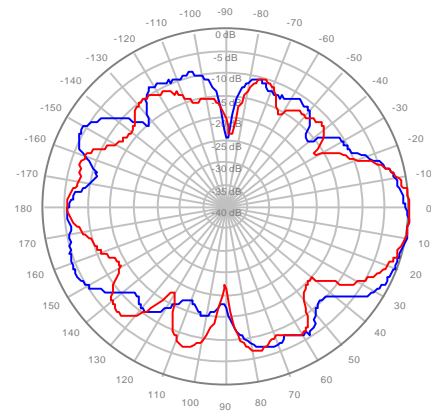
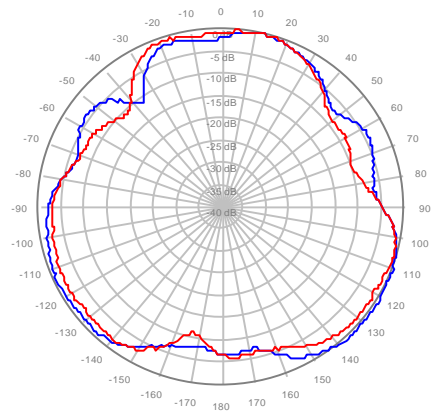
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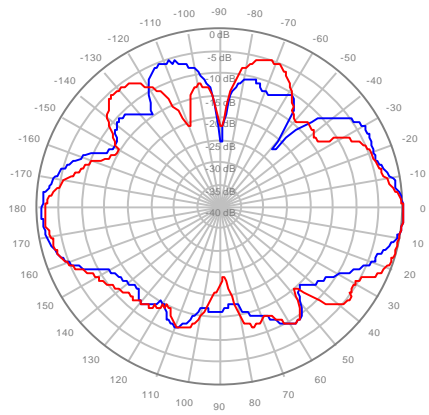
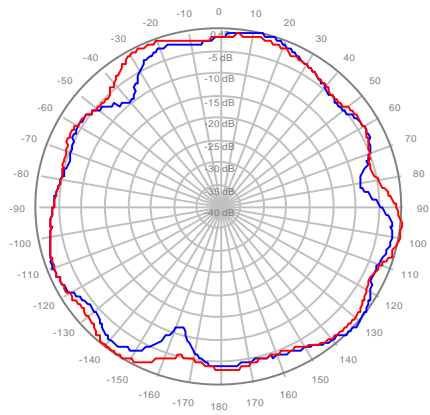
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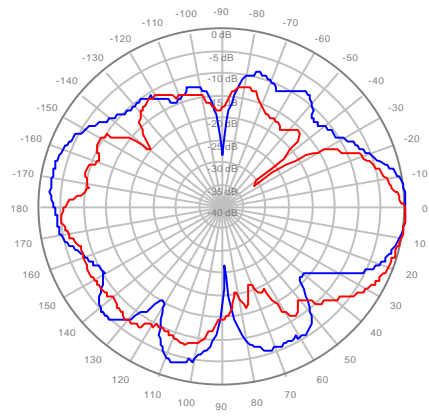
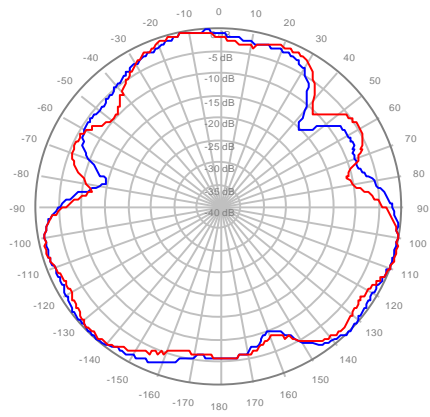
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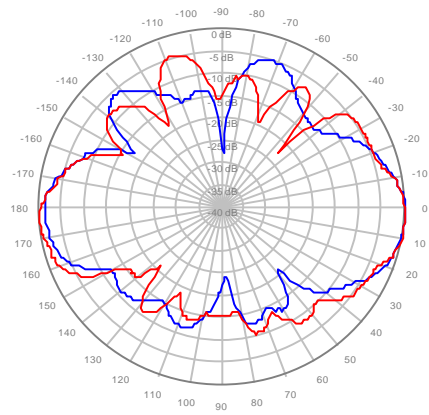
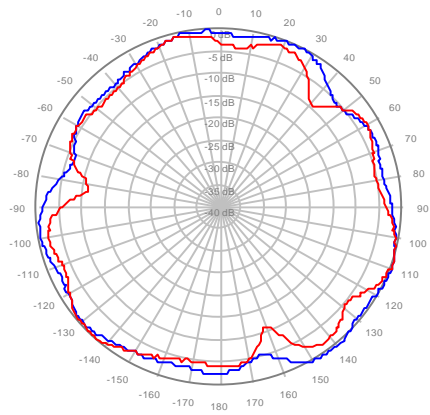
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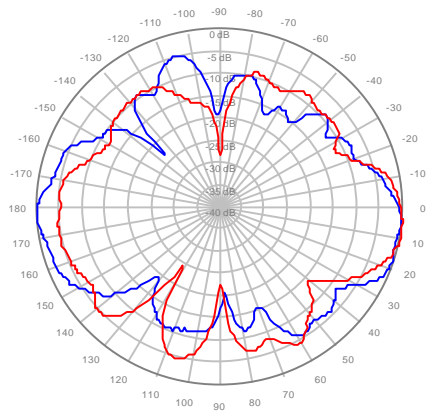
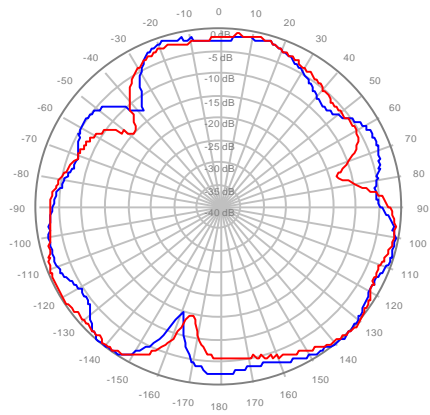
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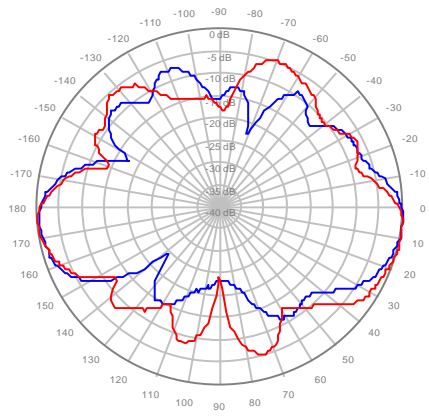
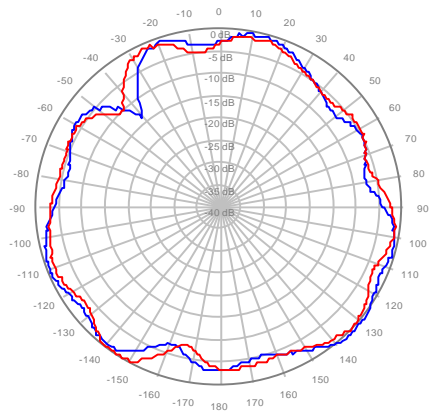
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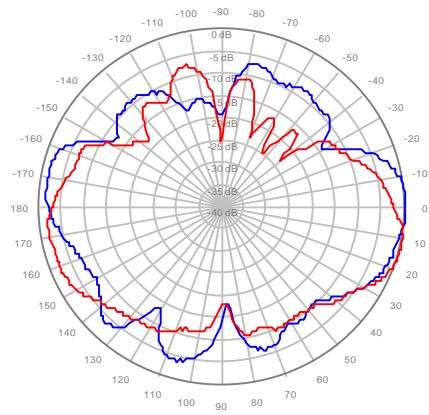
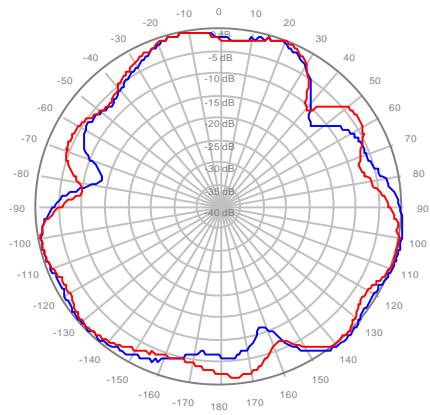
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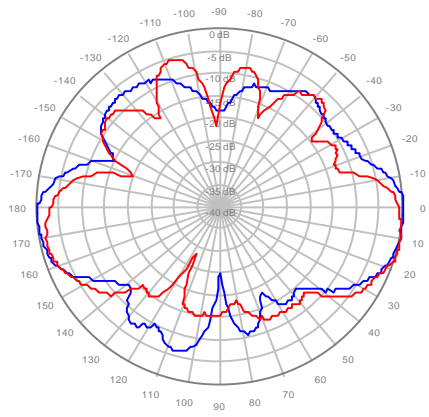
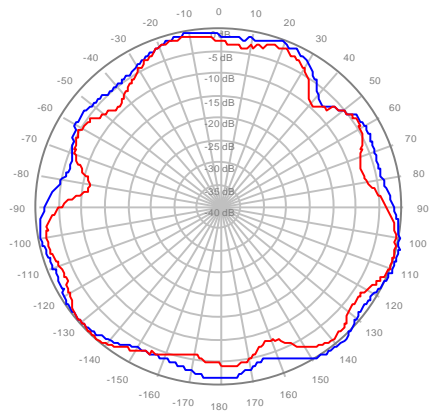
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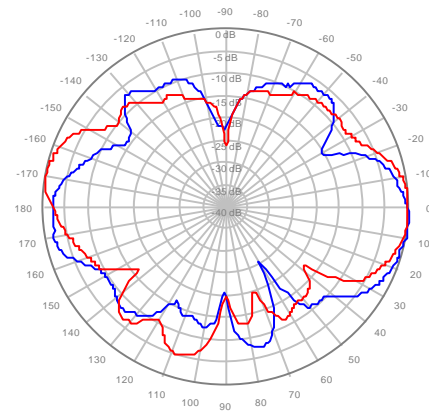
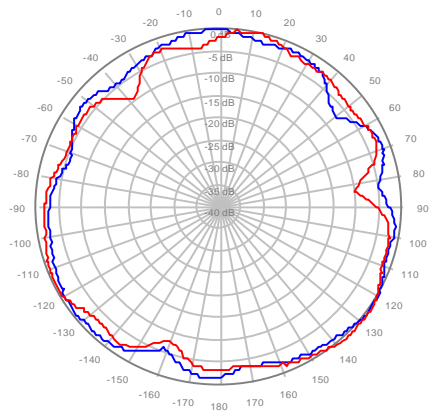
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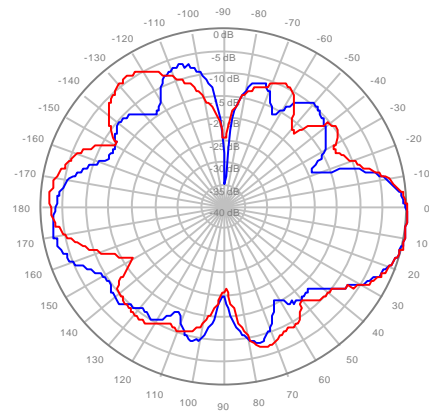
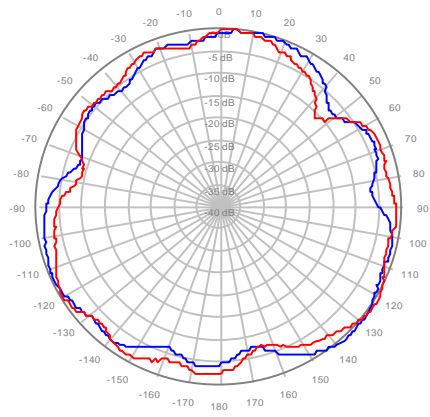
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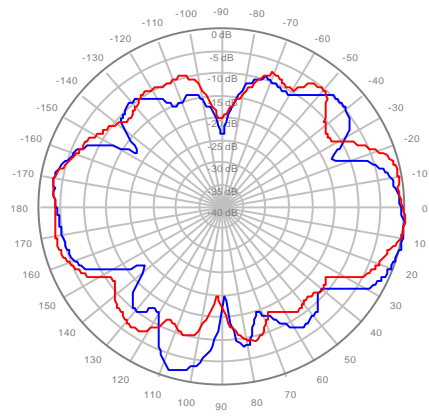
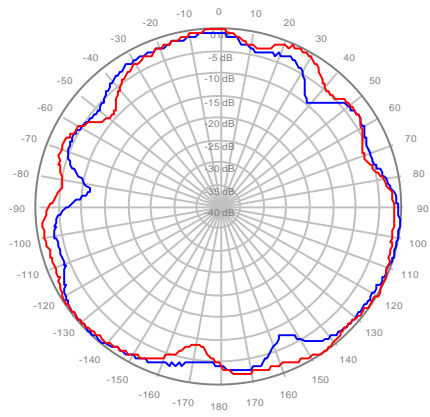
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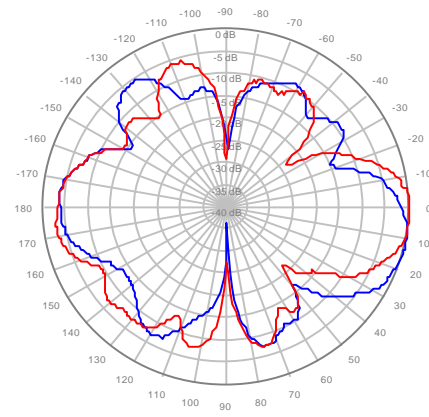
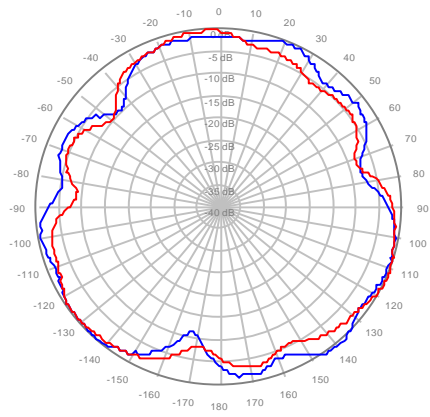
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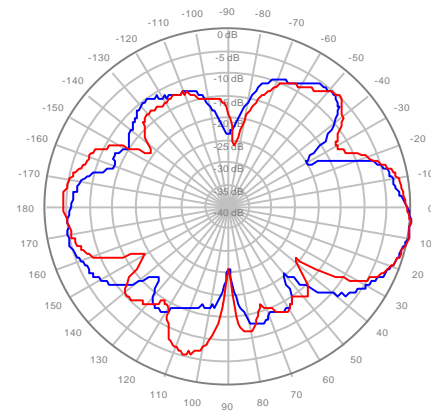
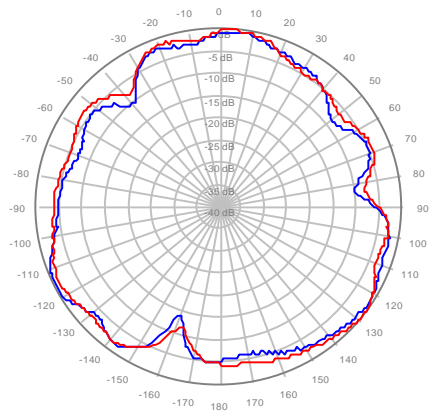
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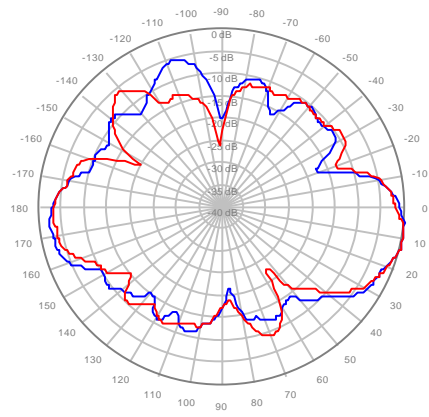
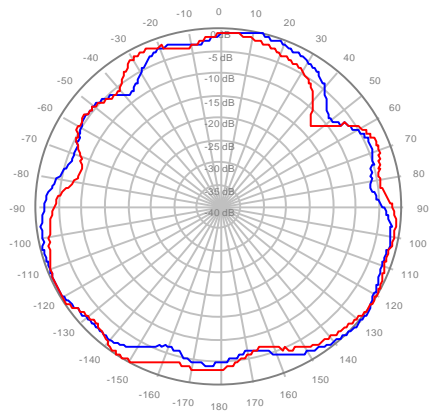
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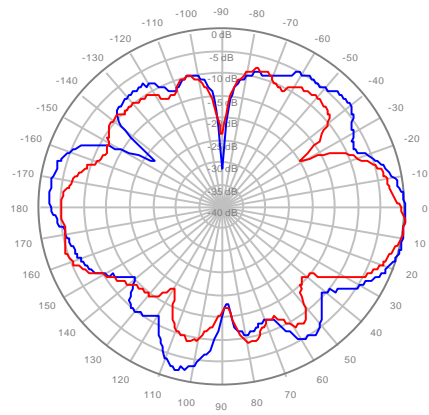
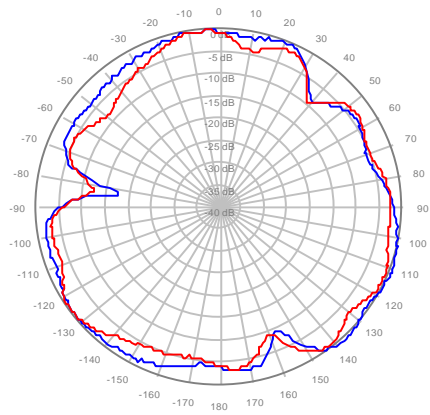
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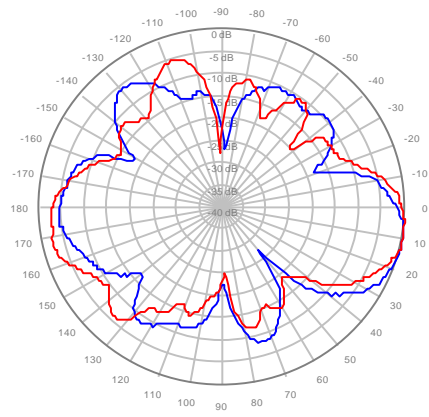
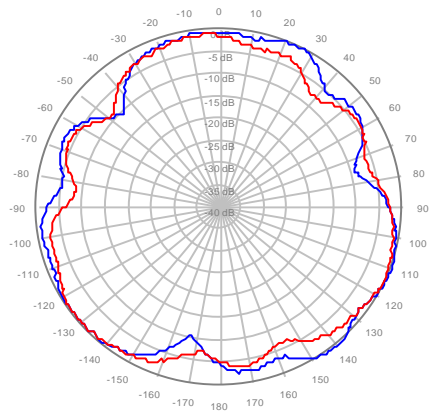
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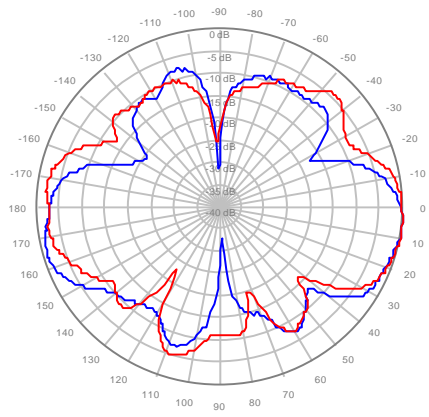
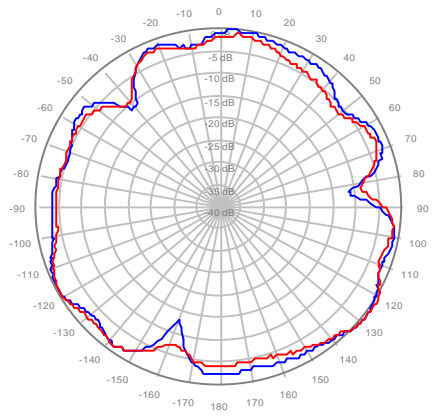
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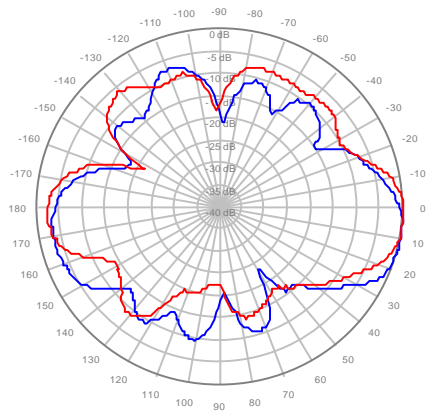
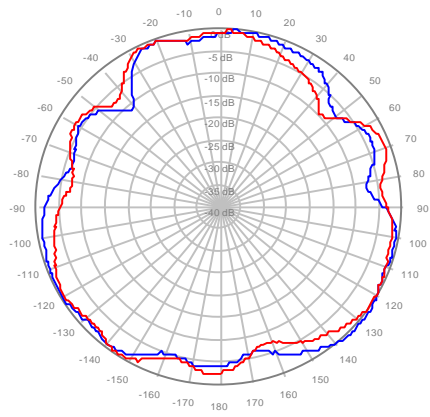
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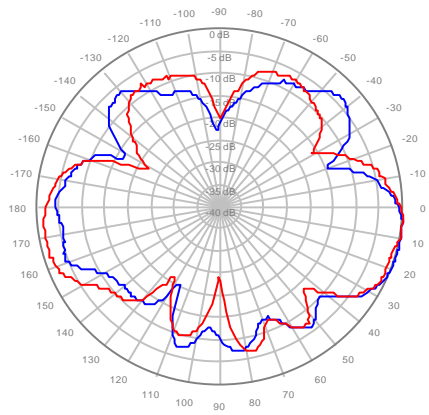
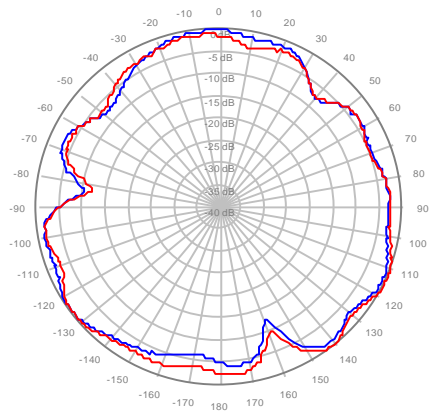
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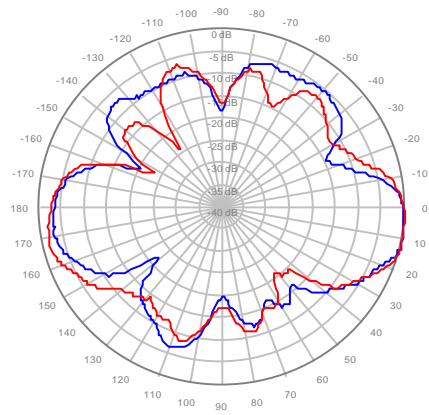
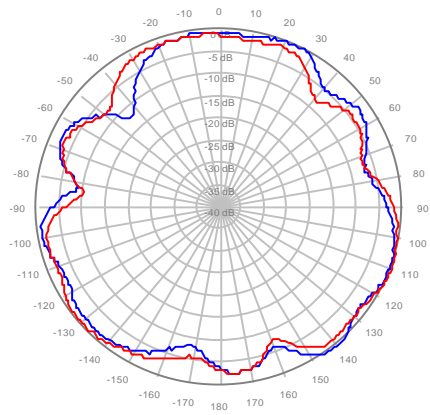
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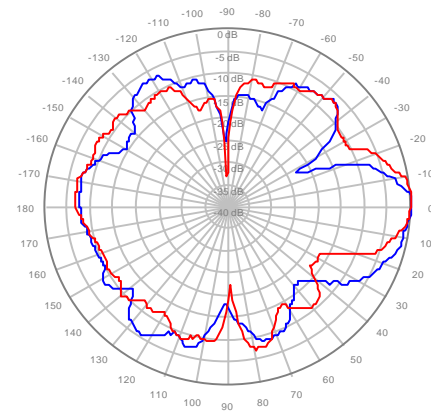
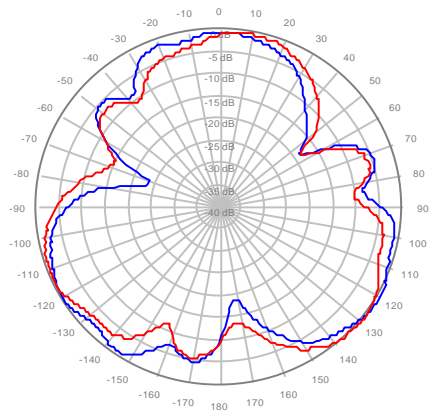
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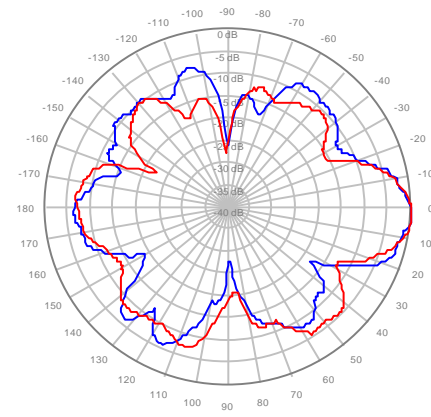
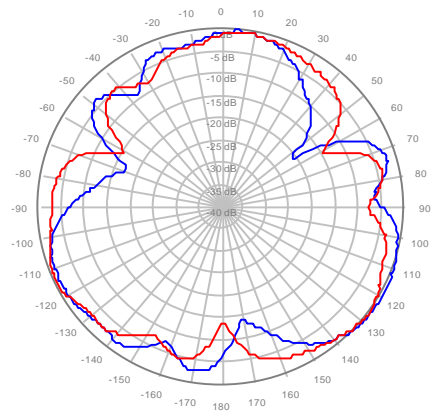
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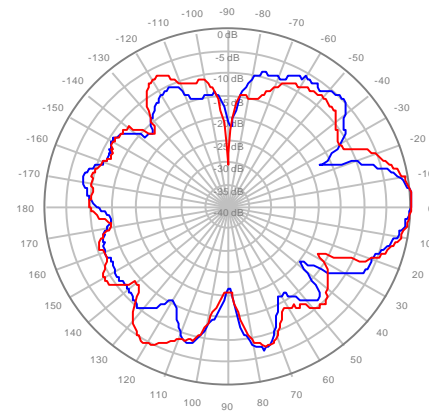
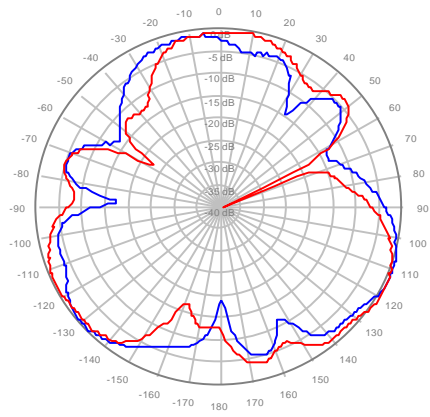
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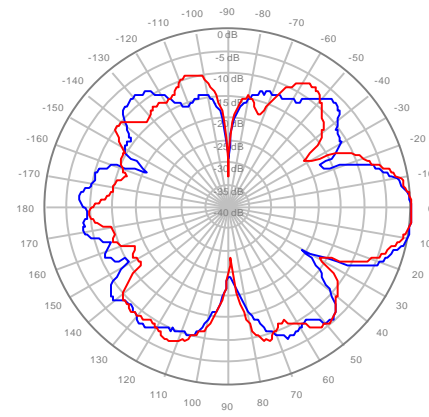
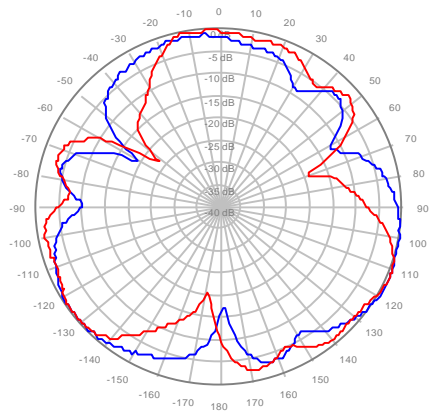
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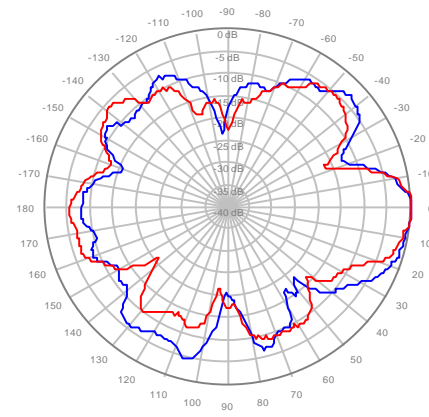
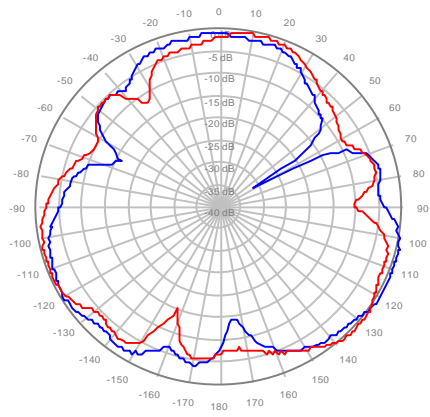
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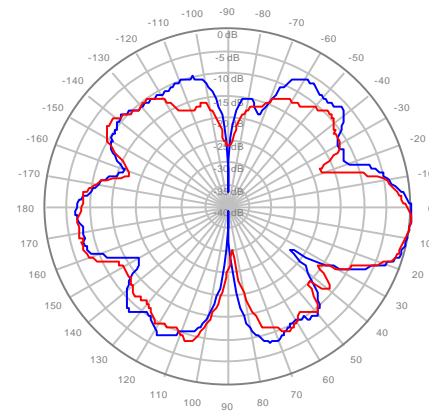
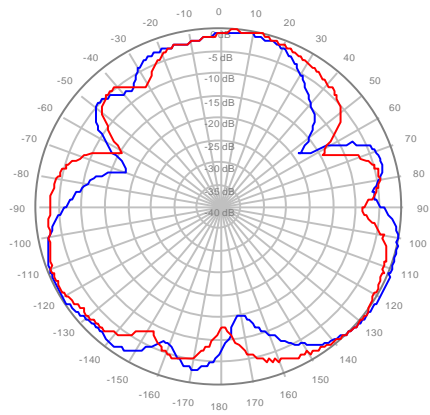
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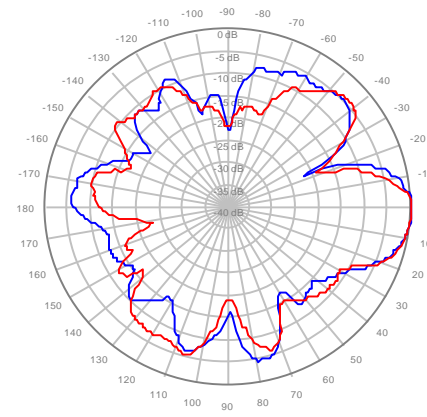
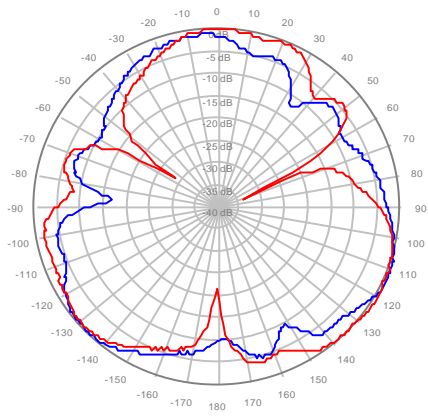
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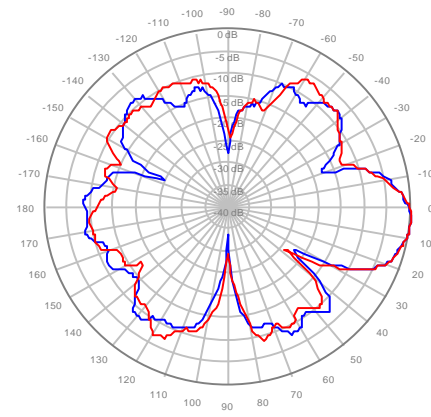
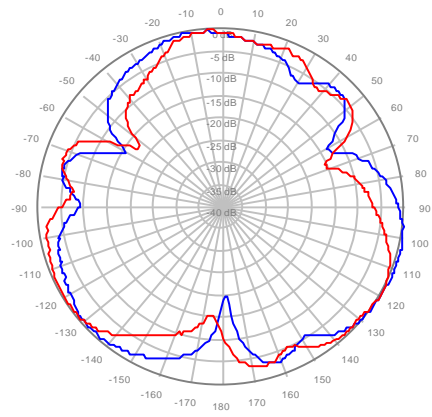
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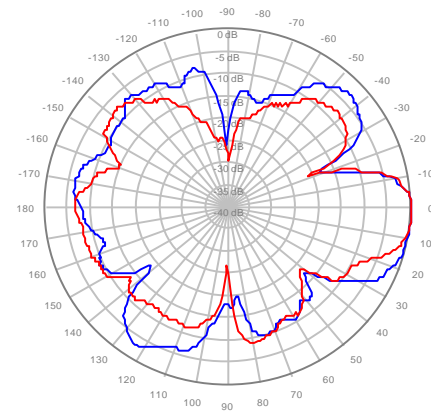
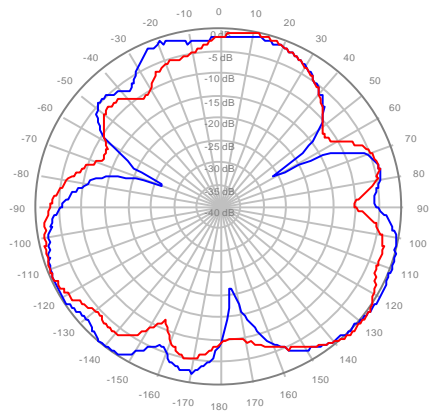
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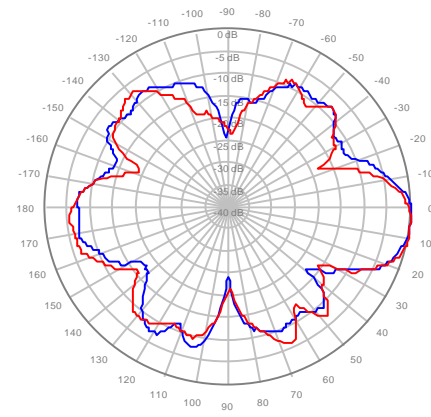
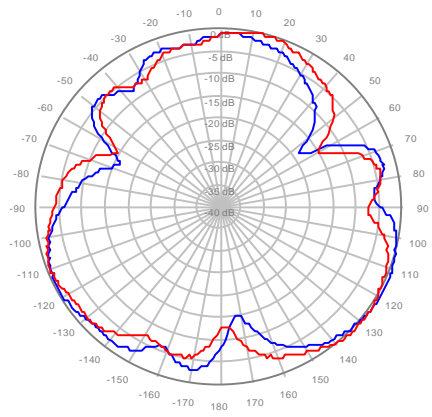
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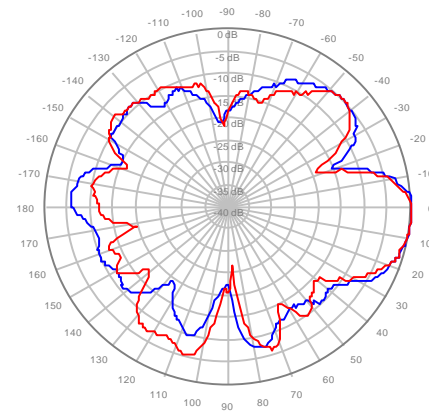
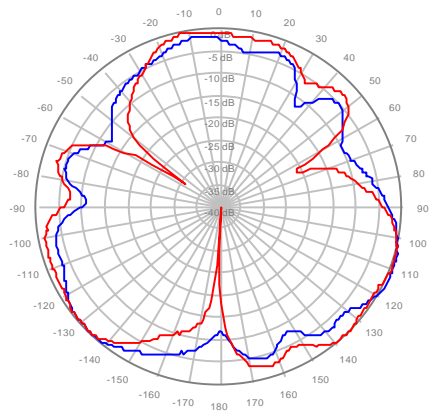
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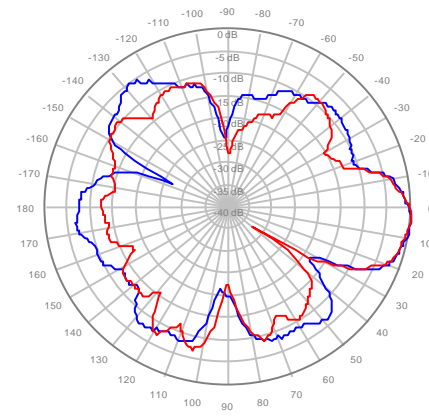
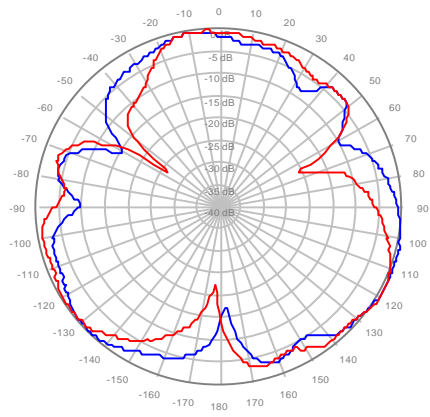
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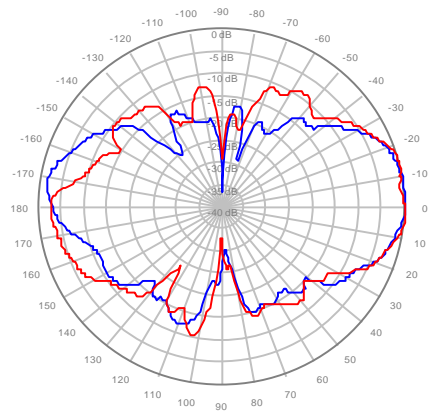
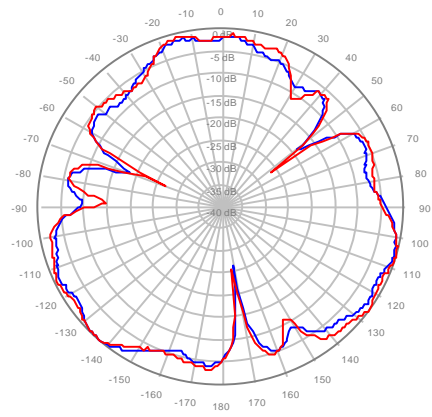
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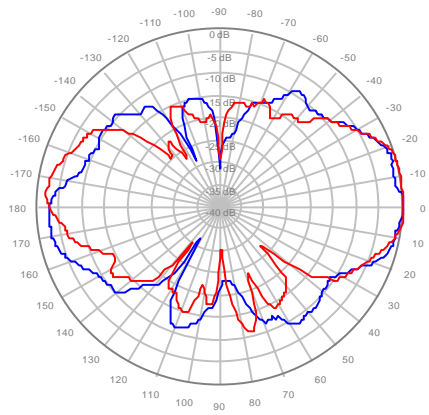
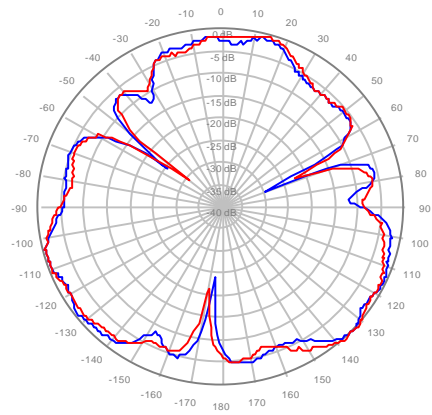
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2C4U3MT360X06F00S0 P1, ±Ports 3550-3700MHz



2C4U3MT360X06F00S0 P2, ±Ports 3550-3700MHz



2C4U3MT360X06F00S0 O1, ±Ports 5150-5925MHz

